

FORM PTO-892 (REV. 2-92)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		SERIAL NO. <b>09-805,103</b>	GROUP/ART UNIT <b>2817</b>	ATTACHMENT TO PAPER NUMBER <b>9</b>			
NOTICE OF REFERENCES CITED				APPLICANT(S) <b>Tomita</b>					
U.S. PATENT DOCUMENTS									
*		DOCUMENT NO.	DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE		
	A	Re 31545	3-27-1984	Quinn					
	B								
	C								
	D								
	E								
	F								
	G								
	H								
	I								
	J								
	K								
FOREIGN PATENT DOCUMENTS									
*		DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUB-CLASS	PERTINENT SHTS. DWG.	PP. SPEC.
	L	5291850	11-5-1993	Japan	Katsuharu	330	252		
	M								
	N								
	O								
	P								
	Q								
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)									
	R	SMITH et al. "Evolution of high-speed operational amplifier architectures" Solid-State Circuits, IEEE Journal of, volume: 29, Issue 10, Oct. 1994 pp 1166-1179							
	S								
	T								
	U								
EXAMINER <b>MICHAEL B. STINGLINGTON</b>				DATE <b>9-5-2002</b>					
* A copy of this reference is not being furnished with this office action. (See Manual of Patent Examining Procedure, section 707.05 (a).)									